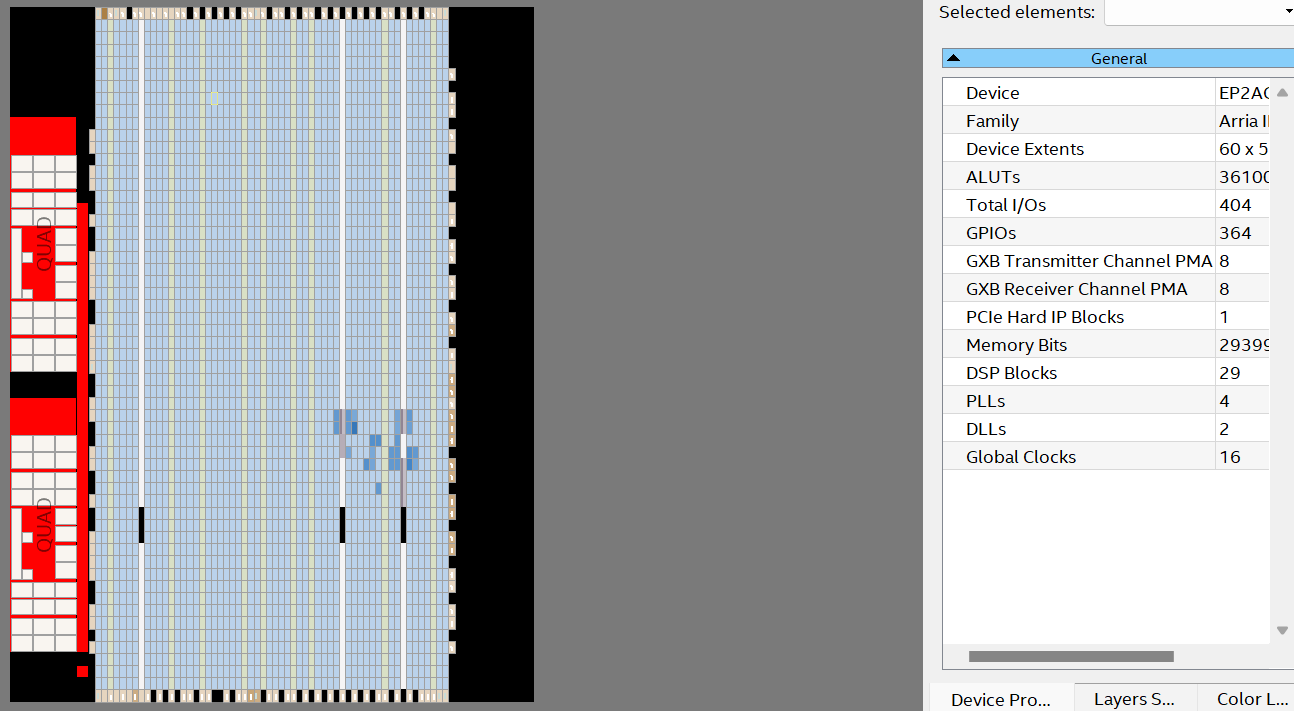
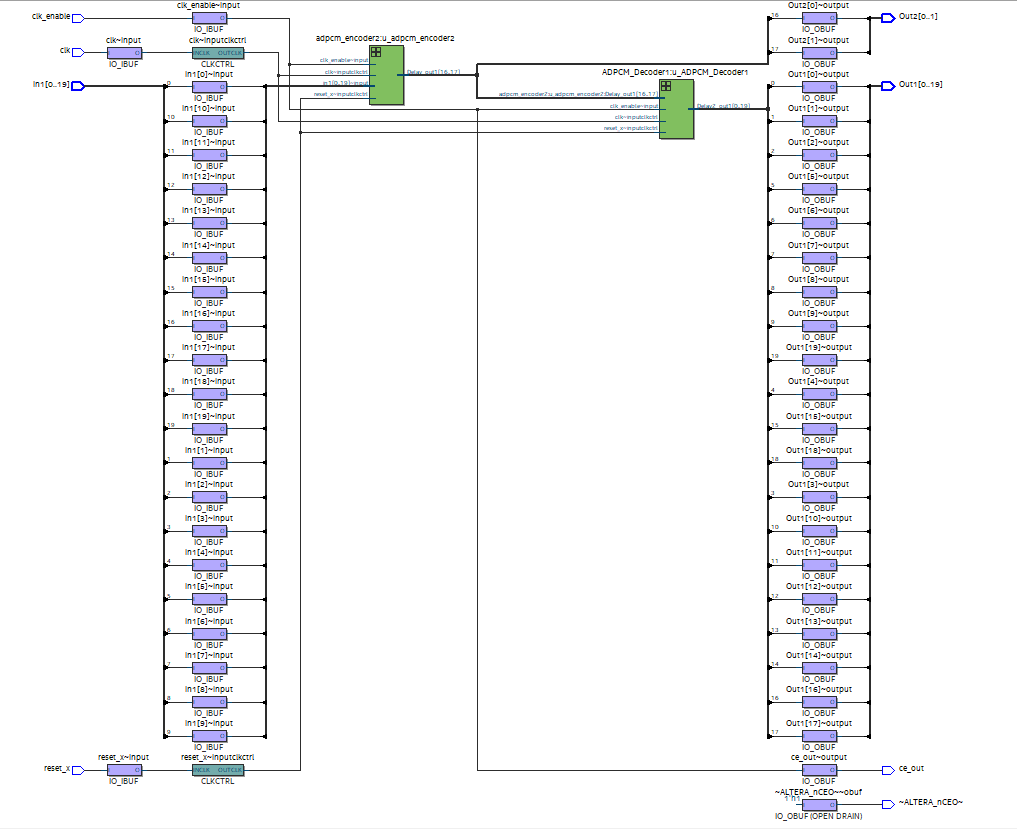
**Baseline Design:**

Chip Planner:

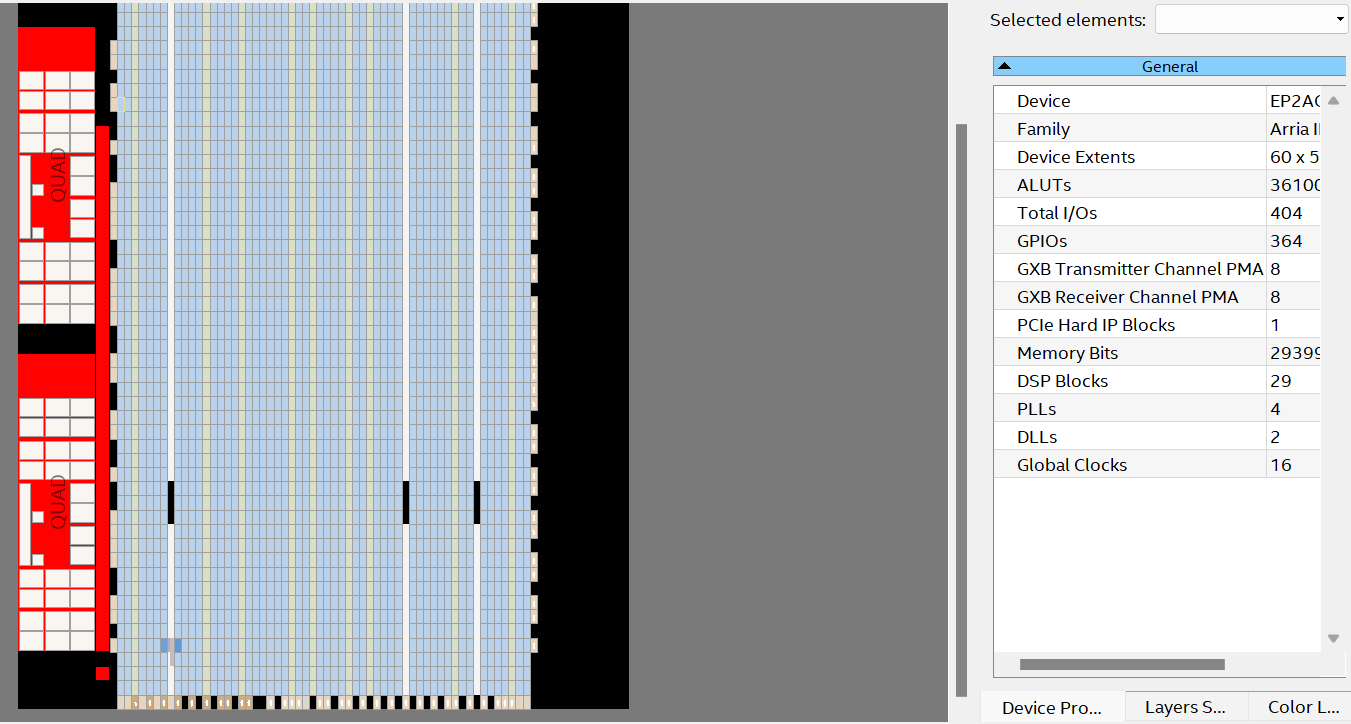


Technology Map Viewer:



**Optimized Design:**

Chip Planner:



Technology Map Viewer:

